

33 36.

(Currently Amended) A computer system, comprising:

a plurality of processors, wherein the processors includes means for issuing a write address separate from data to be written to the write address; and

a shared memory connected to the plurality of processors, wherein the shared memory includes:

means for receiving a first write request including a first write address; and

means for comparing addresses in subsequent load and store requests to the first write address; and

means for stalling subsequent loads and stores load and store requests to a memory location in the shared memory associated with the first write address in shared memory until the data to be written to the write address associated with the first write request is received and written by the shared memory.